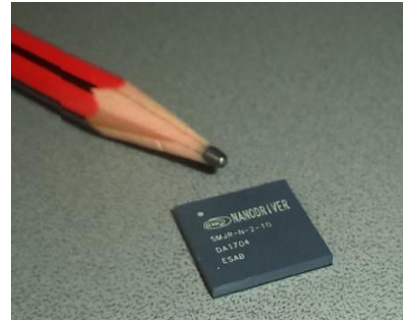


SMJR-N-1-XX - NANODRIVER

16W & 24W 100V LED Driver

NANODRIVER

SMJR-N-1-XX



Product Brief

Description

The Seoul Semiconductor NANODRIVER range of Phase cut drivers are ideal for downlight, spot and track light as well as wall sconce and flush mount fixtures. Based on an incredibly small package, this is ideal for very small custom driver design or for creating AC LED Modules. The NANODRIVER has very low ripple current, enabling easy PSE compliance.

Features and Benefits

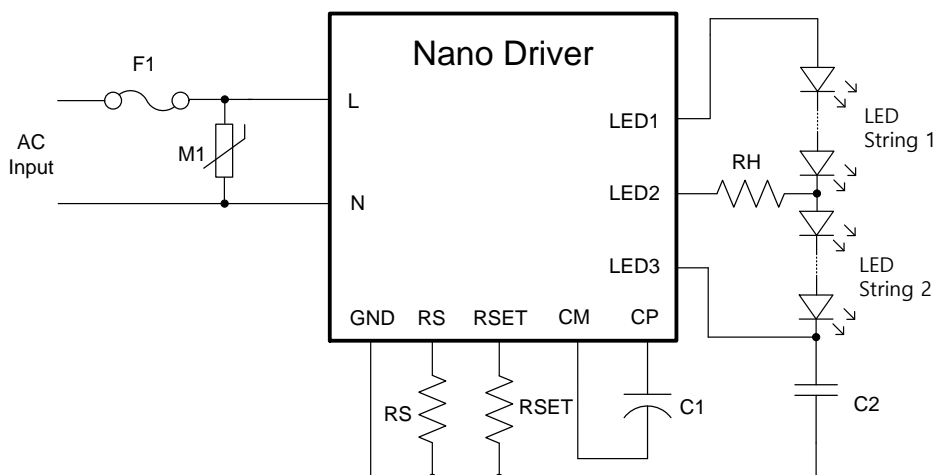
- Very small size 13.5 x 13.5 x 1.424mm
- Low flicker for PSE compliance
- AC Phase cut dimming or analog dimming
- Over temperature protection
- Ultra Low Inrush current
- >0.9 Power Factor
- 5V 20mA Auxiliary Bias Supply

Product Selection

Part No.	Vin [Vac]	P [W]		Remark
		Min.	Max.	
SMJR-N-1-16	100	8	16	780-1850 Lumens Typ.*
SMJR-N-1-24	100	18	24	1770-2860 Lumens Typ.*

Light Output is a for reference guide only. Actual light out will depend on specific LED and BIN selected

Typical Circuit diagram



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Performance Characteristics

Absolute Maximum Ratings, $T_a = 25^\circ\text{C}$

Parameter	Symbol	Unit	Lower Limit	Upper Limit
L,N, LED1, LED2, BLDP, BLDN to GND	-	V	-0.3	450
ADIM, RSET, VAUX, VPS, ISN, RSET,RS1, RS2 to GND	-	V	-0.3	6.5
Continuous Power Dissipation (TA=25°C) ²⁾	P	W	-	4.33
Operating Case Temperature	T_c	°C	-40	120
Operating Ambient Temperature	T_a	°C	-40	70
Storage Ambient Temperature	T_{stg}	°C	-40	120
Maximum Junction Temperature	T_j	°C	-	150
ESD (HBM) ¹⁾	-	kV	-	1.5

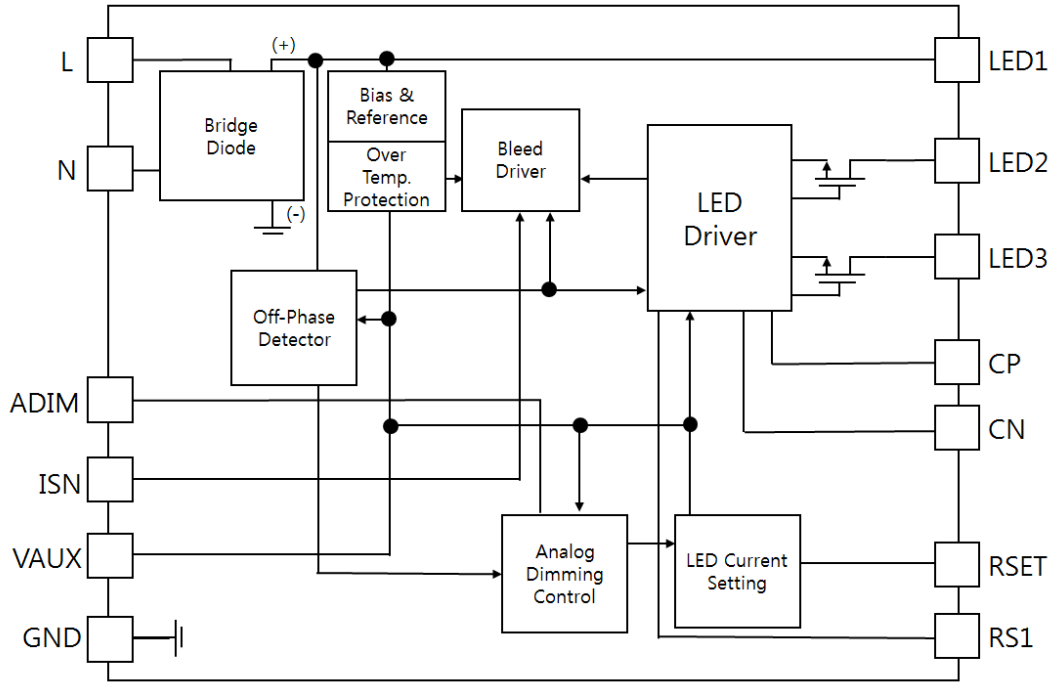
Notes :

Stress beyond those listed Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the following operational sections of the specifications is not implied. Exposure to absolute maximum rating condition(s) for extended periods may affect device reliability.

- 1) Human Body Model (HBM) per JESD22-A114 for all pins.
- 2) Power dissipation is dependent on exact circuit configuration, including input voltage, output power, auxiliary power consumption etc. Expected Power dissipation is provided in detailed power dissipation curves below.

Block Diagram

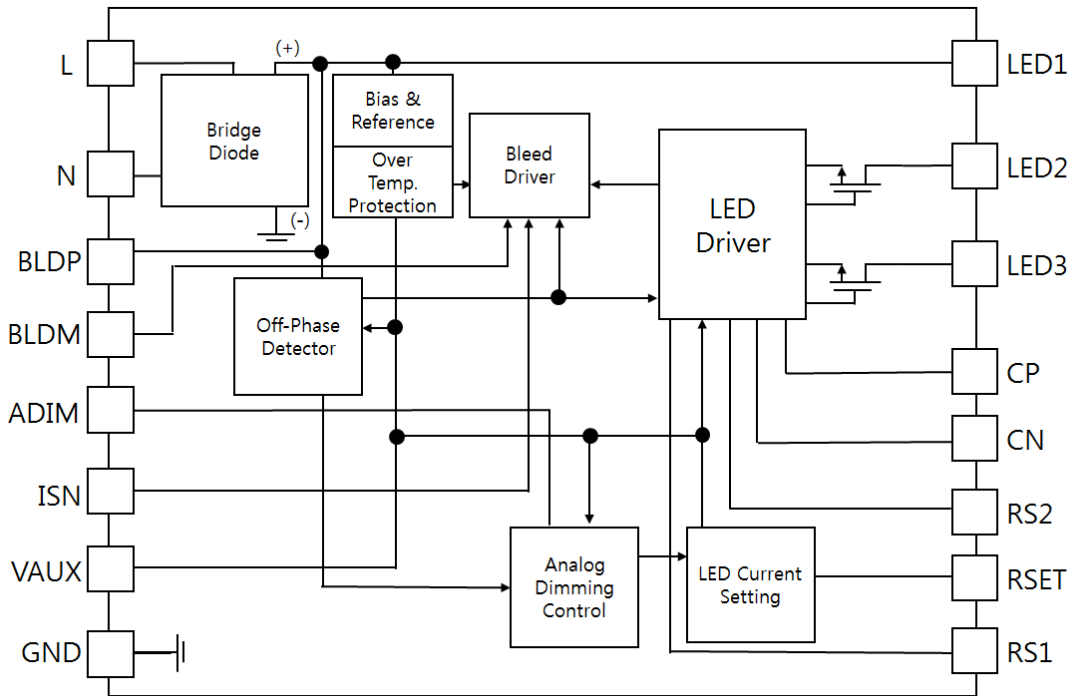
SMJR-N-1-16 upto 16W with internal bleeder.



Pin	Name	Description
1	L	AC Input Live
2	N	AC Input Neutral
3	ADIM	Analog Dimming (0.45V ~ 3V)
4	ISN	Ground
5	VAUX	Auxiliary Power Supply Output(5V 20mA)
6	GND	Voltage Output (-)
7	RS1	L1 Current Setting
8	RSET	Total Current Setting
9	CN	Capacitor (-)
10	CP	Capacitor (+)
11	LED3	LED (-)
12	LED2	LED middle
13	LED1	LED (+)

Block Diagram

SMJR-N-1-24 16-24W with external bleeder resistor.



Pin	Name	Description
1	L	AC Input Live
2	N	AC Input Neutral
3	BLDP	Bleed Resistor
4	BLDM	Bleed Resistor
5	ADIM	Analog Dimming (0.45V ~ 3V)
6	ISN	Ground
7	VAUX	Auxiliary Power Supply Output(5V 20mA)
8	GND	Voltage Output (-)
9	RS1	L1 Current Setting
10	RSET	Total Current Setting
11	RS2	L1 Current Setting
12	CN	Capacitor (-)
13	CP	Capacitor (+)
14	LED3	LED (-)
15	LED2	LED middle
16	LED1	LED (+)

Device Functional Description

Topology and Fundamental Operation

The NANODRIVER uses a patented Seoul Semiconductor AC Topology using low frequency current steering without the use of magnetic components. During the peaks of the sine wave energy is stored in an electrolytic capacitor. The stored energy is used to power the LEDs during the trough between the peaks to provide an equivalent dc flux to provide very lower measured ripple. The NANODRIVER supports both leading Edge and Trailing edge dimmer compatibility or can be configured for analog dimming such as 0-10V dimming.

Bleeding Function

The NANODRIVER includes a bleeding functions which enables operation with multiple dimmers. In Analog Dimming Mode, bleeding functions are disabled, and bleeding functions are enabled in TRIAC dimmer mode only. The main functions provide the dimmers with sufficient current to maintain the minimum holding current for various TRIACs and to simulate filament turn on current for some active smart dimmers in common use. A list of tested dimmers is provided below.

Thermal Protection

The NANODRIVER includes a thermal shutdown function. This protection protects the NANODRIVER from overheating caused by excessive power dissipation. An internal temperature sensor continuously monitors the junction temperature. If junction temperature exceeds about 160°C, the LED current will be reduced to the half of the total current.

RSET Open Protection

When the R_{SET} voltage exceeds R_{SET} open detection voltage V_{RSETP} (typ. 2V), all LED drivers are disabled. And all LED drivers will resume operation when the R_{SET} voltage falls below V_{RSETN} (typ. 1.5V). There is a typical 0.5V hysteresis for reliable operation.

VAUX Short Protection

The NANODRIVER has the V_{AUX} short circuit protection. When the V_{AUX} voltage falls below the V_{CC} Foldback ON voltage V_{FBON} (typ. 0.5V), the LED current is reduced abruptly and V_{AUX} current is limited by 4mA. If the abnormal condition is removed and the V_{CC} voltage rises above V_{AUX} Foldback OFF voltage V_{FBOFF} (Typ. 1.0V), the LED current and V_{CC} current is re-established. There is a typical 0.5V hysteresis for reliable operation.

RH

RH resisters is used to increase the Power Factor above 0.9. Not using this resistors and connecting directly will result in a higher efficiency and safe operation

Specification

Specification, Ta = 25°C

AC Input Voltage	AC 100V	
Model	SMJR-N-1-16	SMJR-N-1-24
MAXIMUM RATED AVERAGE CURRENT	200mA	300mA
OPERATING VOLTAGE	Led String 1 =34V, Led String 2 =51V	
CURRENT ACCURACY	±5%	
FLICKER	Less Than 10%	
STARTUP TIME	<300ms	
FREQUENCY RANGE	50~60Hz	
POWER FACTOR(Typ.)	PF > 0.9	
INRUSH CURRENT(max.)	<30mA	
LINE REGULATION	±10% at AC ±10%	

Performance Characteristics

Electrical Characteristics, $V_{VP} = 50V$, $T_a = 25^\circ C$

Parameter	Symbol	Condition	Value			Unit
			Min.	Typ.	Max.	
GENERAL						
Operating Range	V_{S_VP}		15		400	V
VDD Turn On Threshold Voltage ¹⁾	V_{VDD_ON}	V_{VCC} rising	3.0	3.6	4.4	V
VDD Hysteresis Voltage ¹⁾	V_{VDD_HYS}	$V_{VDD_OFF} = V_{VDD_ON} - V_{VDD_HYS}$	-	0.4	-	V
VP Operating Current	I_{OPR}	$V_p=50V$	-	900	1300	uA
VAUX Auxiliary Output	V_{VCC}	$I_{LOAD}=0mA$	5.2	6.0	6.2	V
	V_{VCC2}	$I_{LOAD}=10mA$		5.4		V
	V_{VCC3}	$I_{LOAD}=20mA$		5.0		V
RSET Current	I_{RSET}	At normal condition	148	155	162	uA
RSET Current at Maximum Scaling	I_{RSCL}	At maximum scaling	5	15	30	uA
PROTECTIONS						
Thermal Shutdown Temperature ²⁾	TSD	Temperature latch off Recycle power to recover		160		°C
RSET Open Detection Positive Voltage	V_{RSETP}	VRSET rising	1.7	2.0	2.3	V
RSET Open Detection Hysteresis Voltage	V_{RSET_HYS}	$V_{RSETN} = V_{RSETP} - V_{RSET_HYS}$	-	0.5	-	V
VAUX Foldback On Voltage	V_{FBON}	V_{VCC} falling	0.2	0.5	0.7	V
VAUX Foldback Hysteresis Voltage	V_{FB_HYS}	$V_{FBOFF} = V_{FBON} + V_{FB_HYS}$	-	0.5	-	V
VAUX Foldback Current	I_{FB}		3	4	5.5	mA

Performance Characteristics

Electrical Characteristics, $V_{VP} = 50V$, $T_A = 25^\circ C$

Parameter	Symbol	Condition	Value			Unit
			Min.	Typ.	Max.	
ANALOG DIMMING CONTROLLER						
ADIM Off Voltage	V_{ADIM_OFF}	RSET=2k Ω , ADIM falling	310	400	470	mV
ADIM Off Hysteresis Voltage	V_{ADIM_HYS}	$A_{DIM_ON} = V_{ADIM_OFF} + V_{ADIM_HYS}$	-	050	-	mV
ADIM Clamp Voltage Range	V_{ADIM_CL}	ADIM sweep	-	3.0	-	V
BLEEDING CURRENT DRIVER						
Week Bleeding Current	I_{BLD_WK}		60	73	80	mA
Startup Bleeding Current	I_{BLD_SS}	Bleeding current at startup	30	32	40	mA
Maximum Bleeding Current	I_{BLD_MAX}		40	45	50	mA
SCALING						
Scaling Start Angle ²⁾	D_{START}		-	120	-	Deg.
Scaling End Angle ²⁾	D_{END}		-	90	-	Deg.

Notes :

$V_{VP} = 50V$, $T_A = -40^\circ C \sim 85^\circ C$). Typical values are at $T_A = 25^\circ C$, unless otherwise specified.

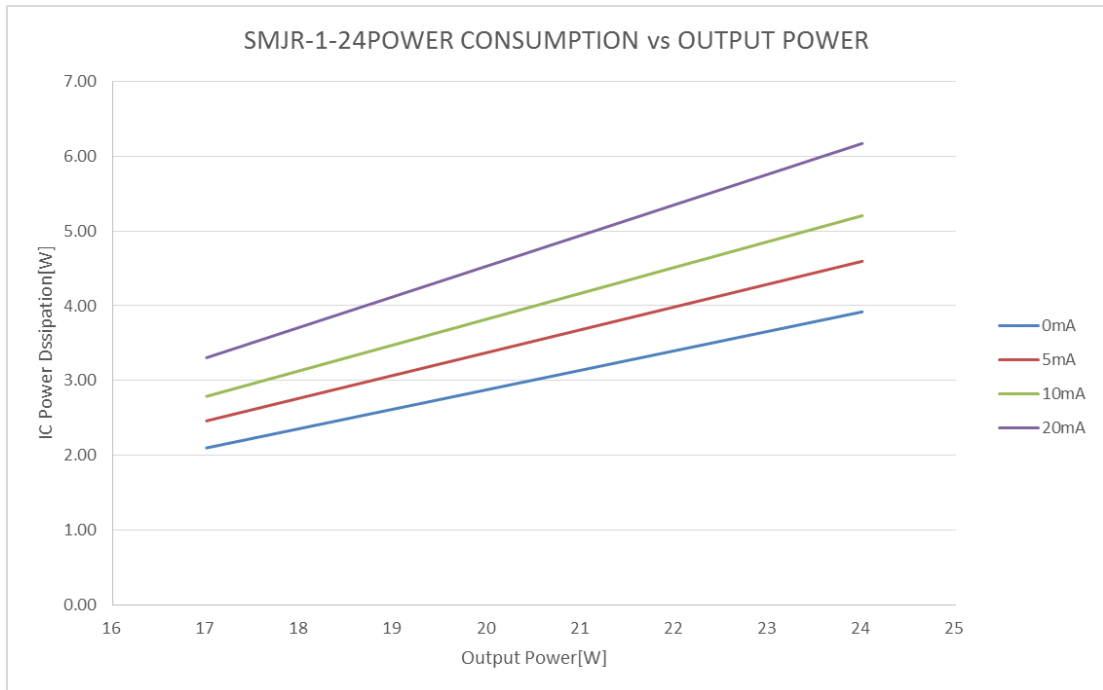
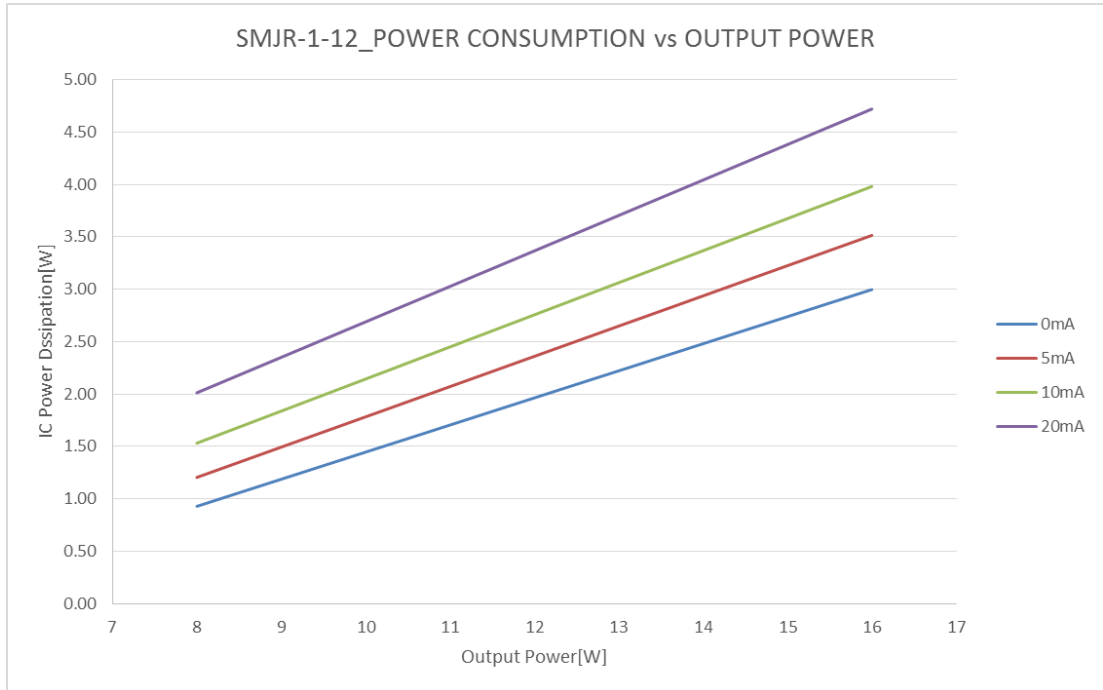
¹⁾ Specifications over the T_A range are assured by design, characterized and correlated with process control.

1) The VDD is internal power supply for internal block.

2) Guaranteed by design, characterization and correlation with process controls. Not fully tested in production.

Characteristics Graph

IC Power Dissipation



Thermal Resistance

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance Junction to Ambient ^{1), 2), 3)}	Θ_{JA}	28.9	°C/W
Thermal Resistance Junction to Case, Top ^{1), 2), 4)}	Θ_{Jc}	14.8	°C/W

Notes :

1) $T_A = 25\text{ }^\circ\text{C}$

2) Measured in still air-free convection condition (conforms to EIA/JESD51-2) on high effective thermal conductivity JESD51-7 test board.

$$3) \Theta_{JA} = (T_{J,max} - T_A) / P_{D,max}$$

where, $T_{J,max}$, T_A , Θ_{JA} and $P_{D,max}$ are maximum junction temperature, ambient temperature, junction-to-ambient thermal resistance and maximum power dissipation respectively. This conforms to JESD51-12.

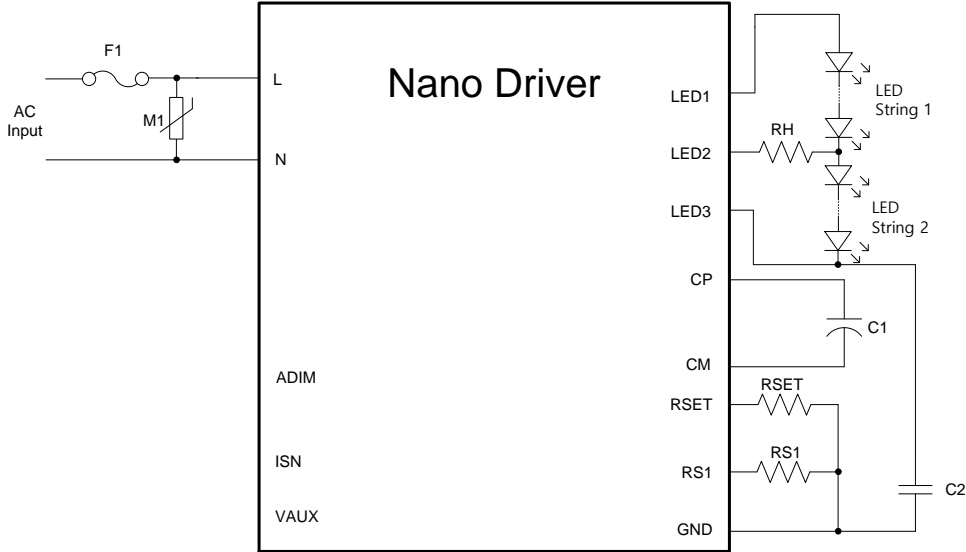
$$4) \Theta_{JcX} = (T_J - T_c) / P$$

where, T_J , T_c , Θ_{JcX} and P are junction temperature, case temperature, junction-to-case thermal resistance and the part of the chip power that flows from junction to the "x" case surface respectively.

And the "x" indicates the case surface where TC is measured and through which the heat is forced to flow during the Θ_{JcX} measurement, "TOP" for the top surface or "BOT" for the bottom surface. This conforms to JESD51-12.

Operating Circuit

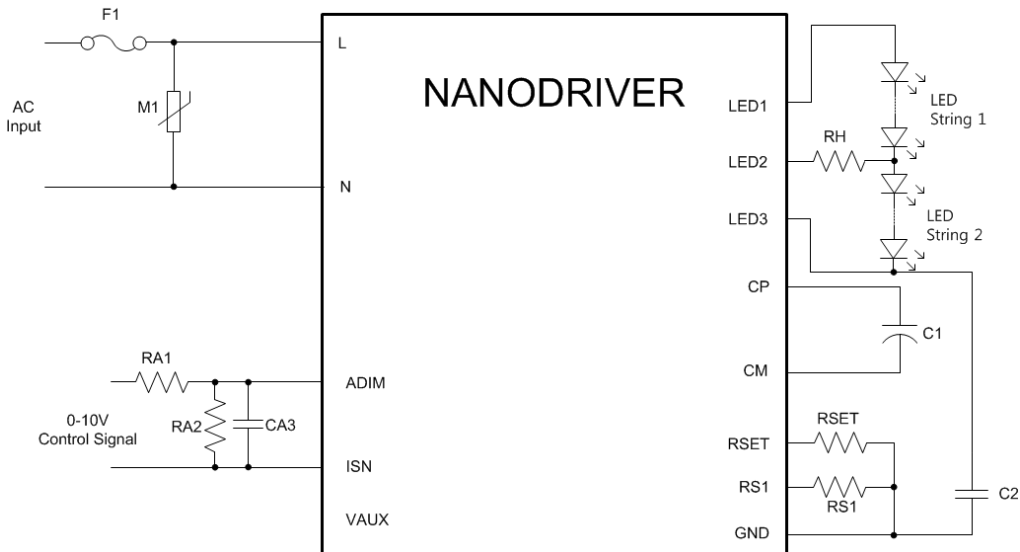
8-16W Phase Cut Dimming



Notes :

- 1) RH is optional. Is required for obtaining the highest power factor.
- 2) EMI can be improved by adding C2.

8-16W 0-10V Dimming

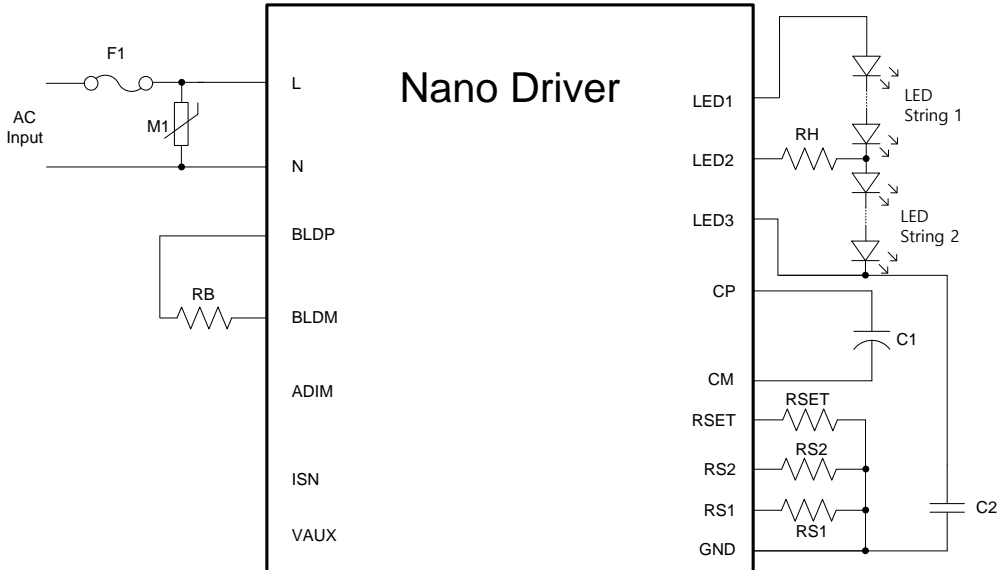


Notes :

- 1) The NANODRIVER is a non-isolated topology and the 0-10V signal needs to be isolated. VAUX is a bias supply that may be used to assist in isolating the source.

Operating Circuit

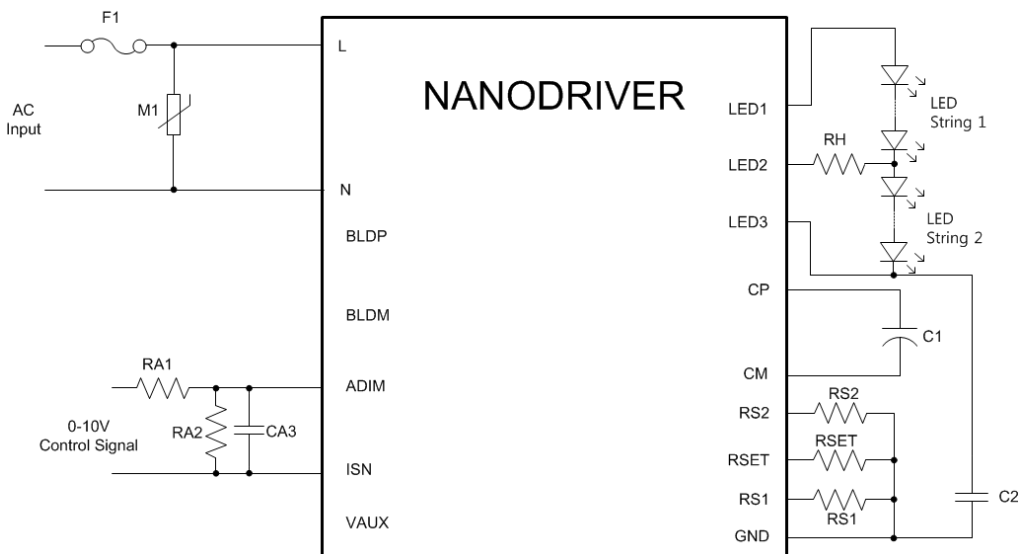
16-24W Phase Cut Dimming



Notes :

- 1) RH is optional. Is required for obtaining the highest power factor.
- 2) EMI can be improved by adding C2.

16-24W 0-10V Dimming



Notes :

- 1) The NANODRIVER is a non-isolated topology and the 0-10V signal needs to be isolated. VAUX is a bias supply that may be used to assist in isolating the source.



LED Array & Component Selection

Led array design

The NANODRIVER can be used with various diode array configurations. In the next few pages we show configurations. Pages 15-17 show a configuration optimized to provide the a very high efficacy operation whilst maintaining PSE compliance.

LED Array & Component Selection

Highest Efficacy Configuration

Component Selection - Typical Values

Power [W]	Io (mA)	C1	C2	RH	RS1	RS2	RSET
8	74	120V 18uF	630V 10nF	30Ω, 1W (5%)	2.7Ω, 0.25W (1%)	-	3.19kΩ, 0.25W (1%)
10	92	120V 18uF	630V 10nF	10Ω, 1W (5%)	2.7Ω, 0.25W (1%)	-	4.00kΩ, 0.25W (1%)
12	110	120V 18uF	630V 10nF	-	2.7Ω, 0.25W (1%)	-	5.10kΩ, 0.25W (1%)
14	128	120V 18uF	630V 10nF	-	2.7Ω, 0.25W (1%)	-	6.13kΩ, 0.25W (1%)
16	145	120V 18uF	630V 10nF	-	2.7Ω, 0.25W (1%)	-	6.85kΩ, 0.25W (1%)
18	166	120V 33uF	630V 10nF	-	2.7Ω, 0.25W (1%)	2.7Ω, 0.25W (1%)	2.12kΩ, 0.25W (1%)
20	184	120V 33uF	630V 10nF	-	2.7Ω, 0.25W (1%)	2.7Ω, 0.25W (1%)	2.24kΩ, 0.25W (1%)
22	202	120V 33uF	630V 10nF	-	2.7Ω, 0.25W (1%)	2.7Ω, 0.25W (1%)	2.44kΩ, 0.25W (1%)
24	220	120V 33uF	630V 10nF	-	2.7Ω, 0.25W (1%)	2.7Ω, 0.25W (1%)	2.72kΩ, 0.25W (1%)

LEDs Selection and Configuration for higher Efficacy

The NANODRIVER output is actually a high voltage waveform in 2 strings. The wave form is using Seoul Semiconductor patented AC waveform with a net light flux that has very low measurable Flicker. For 100Vac input the device is optimized with a String 1 34V and String 2 51V. The converter current is actually divided between the 2 strings with String 1 current measuring approximately 106% of the converter running current and String 2, 80% of the converter running current. In applications such as Linear strips where even brightness between all LEDs is required please consult our Linear Configuration application note.

The NANODRIVER can be used with any LED however the best performance and cost optimization is using Seoul Semiconductor MJT LEDs. For the lowest cost solution the recommended LED to us is the MJT 3528 series. The Following Table provides Part numbers and recommended schematic for connecting the LEDs for each solution:

LED Array & Component Selection

High Efficacy Configuration

Lowest Cost LED Connection

LED	MJT3528 9V Part Number SAW9A32E-E1-ZZ*								
CRI	90								
CCT	2700K								
Power[W]	8	10	12	14	16	18	20	22	24
NANODRIVER	SMJR-N-1-16					SMJR-N-1-24			
Target lumen	785	980	1180	1370	1570	1770	1960	2160	2360
LED Qty. on 1 step	4	4	4	4	4	4	4	4	4
LED Qty. on 2 step	6	6	6	6	6	6	6	6	6
Total[ea]	14	20	20	27	30	34	40	44	50
Schematic	Fig.1	Fig. 2	Fig. 3	Fig. 4	Fig. 5	Fig. 6	Fig. 7	Fig. 8	Fig. 8
Current[mA]	89	110	131	152	172	198	218	240	261
RS1[Ω]	3.3	3.3	3.3	3.3	3.3	3.3	3.3	3.3	3.3
RS2[Ω]						3.3	3.3	3.3	3.3
RH[Ω]	30	10							
RSET[kΩ]	3.19	4.00	5.10	6.13	6.85	2.12	2.24	2.44	2.72

*ZZ is the color temperature eg 2k7=2700CCT, 5k =5000CCT

Light Output is a for reference guide only. Actual light out will depend on specific LED and BIN selected

Lowest Cost LED Connection

LED	MJT3528 9V Part Number SAW8A32E-E1-ZZ*								
CRI	80								
CCT	5000K								
Power[W]	8	10	12	14	16	18	20	22	24
NANODRIVER	SMJR-N-1-16					SMJR-N-1-24			
Target lumen	960	1200	1430	1680	1920	2150	2390	2630	2860
LED Qty. on 1 step	4	4	4	4	4	4	4	4	4
LED Qty. on 2 step	6	6	6	6	6	6	6	6	6
Total[ea]	14	20	20	27	30	34	40	44	50
Schematic	Fig.1	Fig. 2	Fig. 3	Fig. 4	Fig. 5	Fig. 6	Fig. 7	Fig. 8	Fig. 8
Current[mA]	89	110	131	152	172	198	218	240	261
RS1[Ω]	3.3	3.3	3.3	3.3	3.3	3.3	3.3	3.3	3.3
RS2[Ω]						3.3	3.3	3.3	3.3
RH[Ω]	30	10							
RSET[kΩ]	3.19	4.00	5.10	6.13	6.85	2.12	2.24	2.44	2.72

*ZZ is the color temperature eg 2k7=2700CCT, 5k =5000CCT

Light Output is a for reference guide only. Actual light out will depend on specific LED and BIN selected

To adjust the current down for a specific Lumen Target the current can be reduced by changing the value of RSET

SMJR-N-1-16

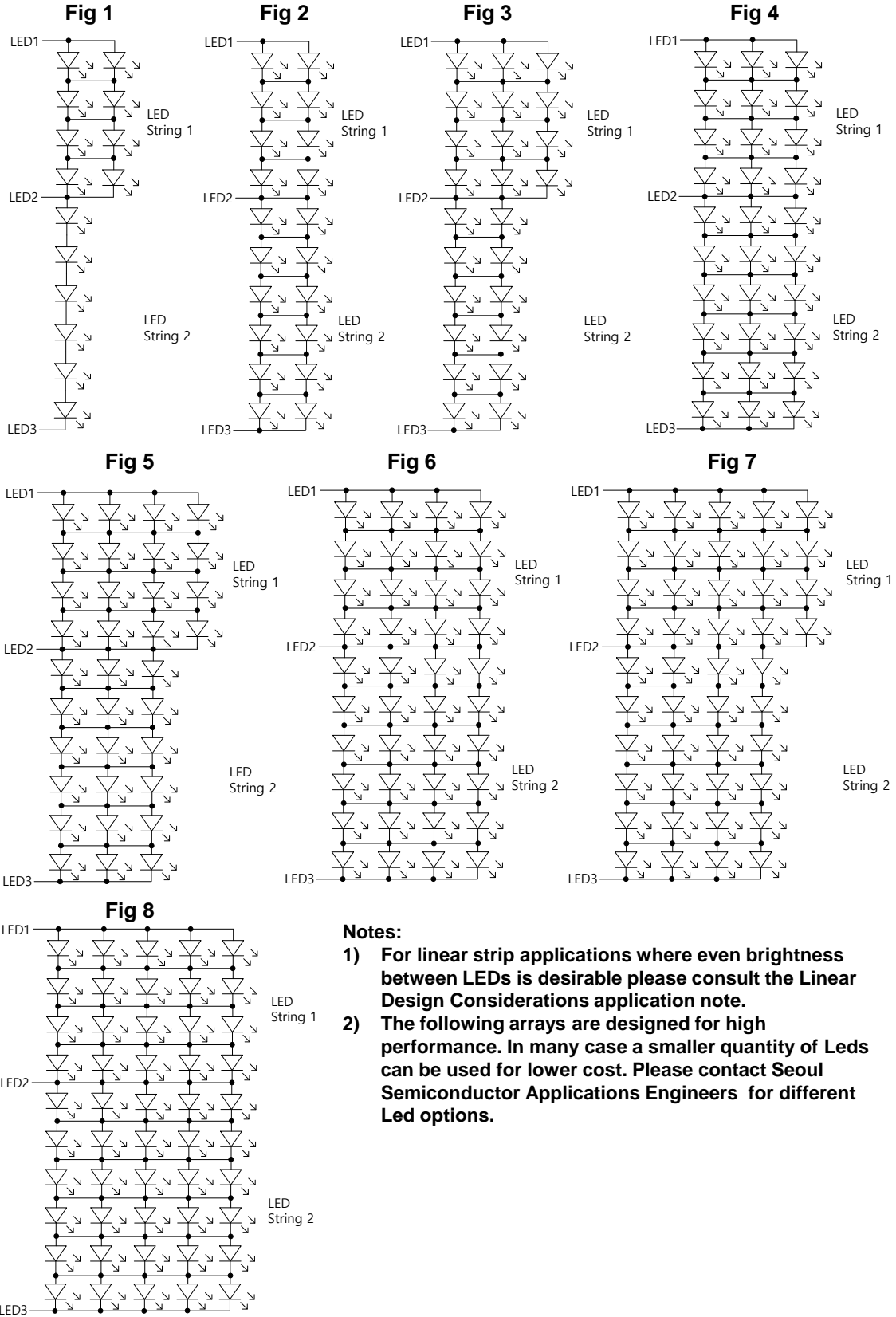
$$R_{set}[k\Omega] = \frac{\text{Current}[A]}{0.0262}$$

SMJR-N-1-24

$$R_{set}[k\Omega] = \frac{\text{Current}[A]}{0.0962}$$

LED Array & Component Selection

High Efficacy Configuration



Notes:

- 1) For linear strip applications where even brightness between LEDs is desirable please consult the Linear Design Considerations application note.
- 2) The following arrays are designed for high performance. In many case a smaller quantity of Leds can be used for lower cost. Please contact Seoul Semiconductor Applications Engineers for different Led options.

LED Array & Component Selection

Component Selection

Fuse F1

Slow-Blow type 250V 1A $I^2T=2.85$

Surge Protection MV1

$V_{rms} = 150V$, $I_{surge, max} = 400A$
recommended TDK / CU3225K150G or equivalent

Bleeder Resistor RB

The Bleeding Function is required for compatibility to various Phase Cut or TRIAC Dimmers. The is required to maintain minimum holding currents and turn-on characteristics. For Power below 16W the Bleeder function is internal to the device. Above 16W an external resistor is required for thermal reasons. For Output Power >16W, use 2 680Ω 1W carbon film resistor in series.

RH

RH is a resistor used to increase the Power factor to >0.9 power factor. Without the resistor in sum conditions the power factor will decrease to 0.85. This component is optional and if high power factor above 0.9 is not required then it may be eliminated and connection can be made directly from LED1 to the LED.

Flicker Capacitor C1

C1 is used to reduce the current ripple in the output LED strings. Any general purpose Aluminum Electrolytic capacitor may be used. Recommended value for <16W is 18uF and 33uF for >16W. Rating for 100V is recommended 120V and ripple current rating 1.3 times the current. Note that Aluminum Electrolytic capacitors do have a shelf life and wear out characteristic that causes the capacitance to decrease over time. This is accelerated with high temperature operation. Please consult with vendor for calculating life and recommendations to meet the expected product life.. The value for Rset will need to change to achieve the same light output. Please contact the factory for recommendations.

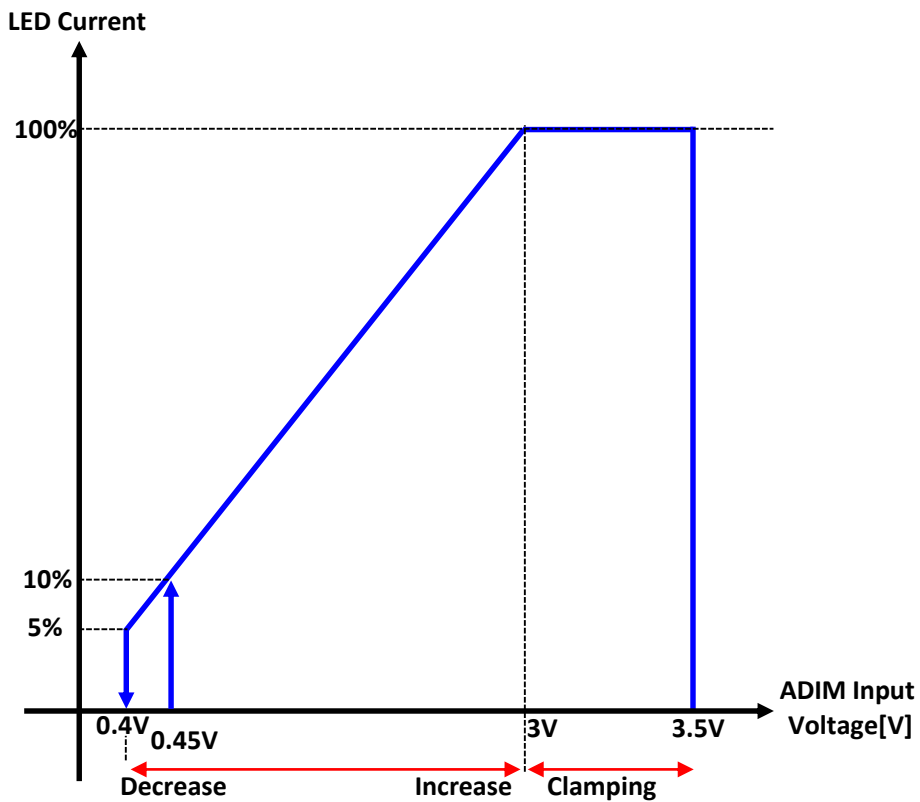
EMI Capacitor C2

This component(630V 10nF X7R type) is only required for Class B conducted EMI. It may be omitted if not required. Increasing this component value can increase EMI margin. But this design may exhibit Audible noise due to the piezoelectric effect on the capacitor for using the TRIAC dimmer.

Analog Dimming

Analog Dimming

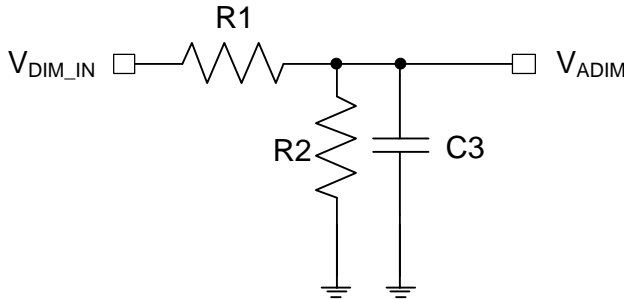
The LED brightness can be adjusted by applying DC voltage to ADIM Pin. When the ADIM DC voltage becomes over 3.0V, the LED brightness is clamped at their maximum flux. When ADIM DC voltage is decreased to 0.4V, LED current becomes off. The voltage has hysteresis to stop flicker and requires the voltage to be raised to 0.45V to turn it back on.



Analog Dimming

External Resistor Setting – For Example

The V_{ADIM} voltage is set using a resistive voltage divider from the V_{DIM_IN} to ADIM pin. The recommended R1 resistor at 10kΩ. Calculate the R2 resistor using the following equation.



$$R2 = \frac{R1 \times V_{ADIM}}{V_{DIM_IN} - V_{ADIM}}$$

Assumptions for the purposes of this example, the following are given as the application requirements:

- V_{DIM_IN} : External dimming voltage signals (0V : Min, 10V : Max)
- R1 : 10kΩ (Recommended value^[1])
- V_{ADIM} : 3V (Constant value^[2])

$$R2 = \frac{R1 \times V_{ADIM}}{V_{DIM_IN} - V_{ADIM}} = \frac{10K\Omega \times 3V}{10V - 3V} = 4.3K\Omega$$

Notes :

- 1) Considering the power consumption of the IC, set the resistance value.
- 2) When the Adim Pin level is 3V, the LED Flux reaches the 100%.

Device Functional Description

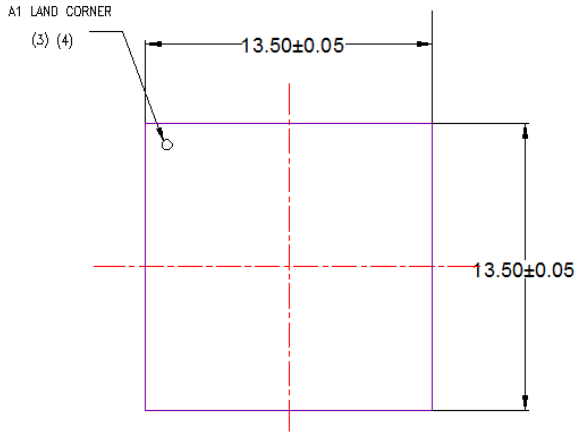
Phase Cut Dimmer Compatibility

Brand	Model	Dimming type	Op. voltage[V]	Dim. Range[%]	Visible Flicker During Dimming			Uniform Dimming
					At Max Level	Min ~ Max	At Min Level	
TOSHIBA	WDG9001	Trailing	100	5-75	No	No	No	OK
Panasonic	WTC57521W	Leading	100	5-78	No	No	No	OK
	WTC57523W	Leading	100	5-78	No	No	No	OK
DNL	BDC300	Leading	100	5-78	No	No	No	OK
Lucon	TRD-303LHA	Leading	100	5-78	No	No	No	OK

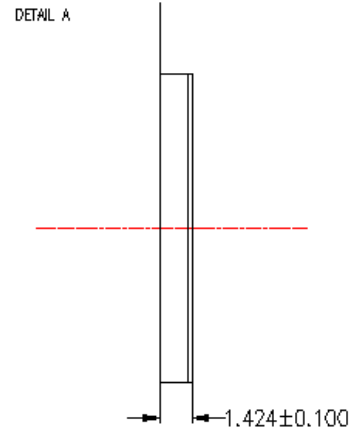
Notes :

- 1) The list includes dimmers tested at time of publishing. This table will be updated from time to time as other dimmers are tested

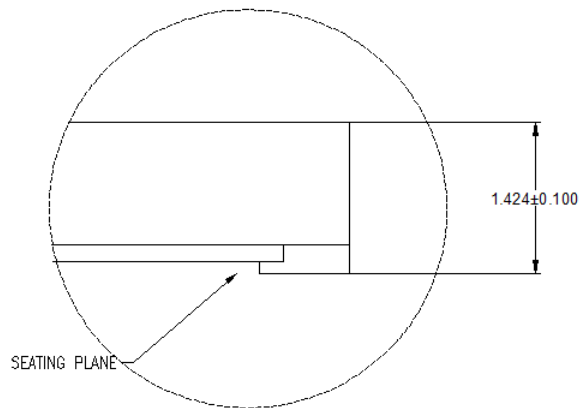
Mechanical Dimensions



<TOP VIEW>



<SIDE VIEW>

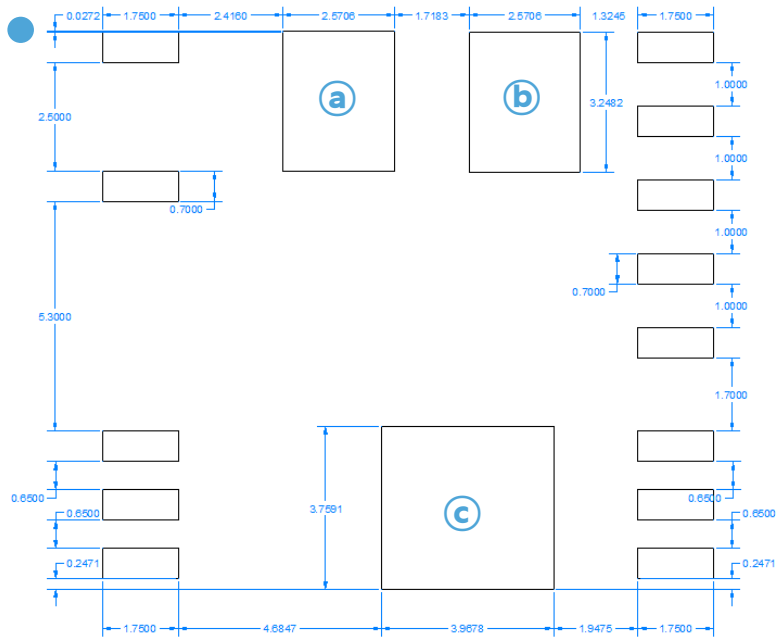


<DETAIL A (Rotated 90°)>

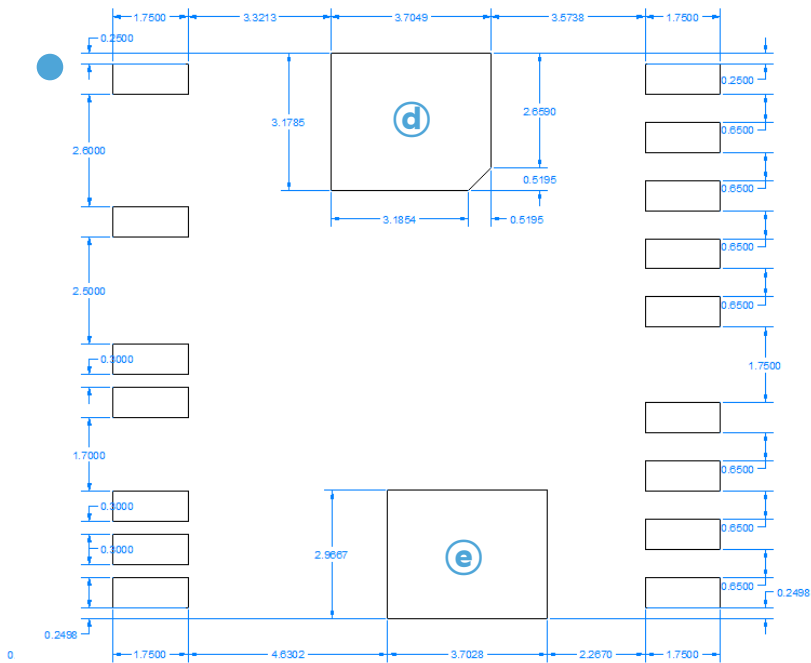
Notes :

- 1) All dimensions and tolerance conform to ASME Y14.5-2009
- 2) Terminal positions designation per JEP 95.
- 3) PIN 1 Identifier can be chamfer, INK mark, LASERED mark, Metallized.

Recommended Solder Pad



<SMJR-N-1-16>

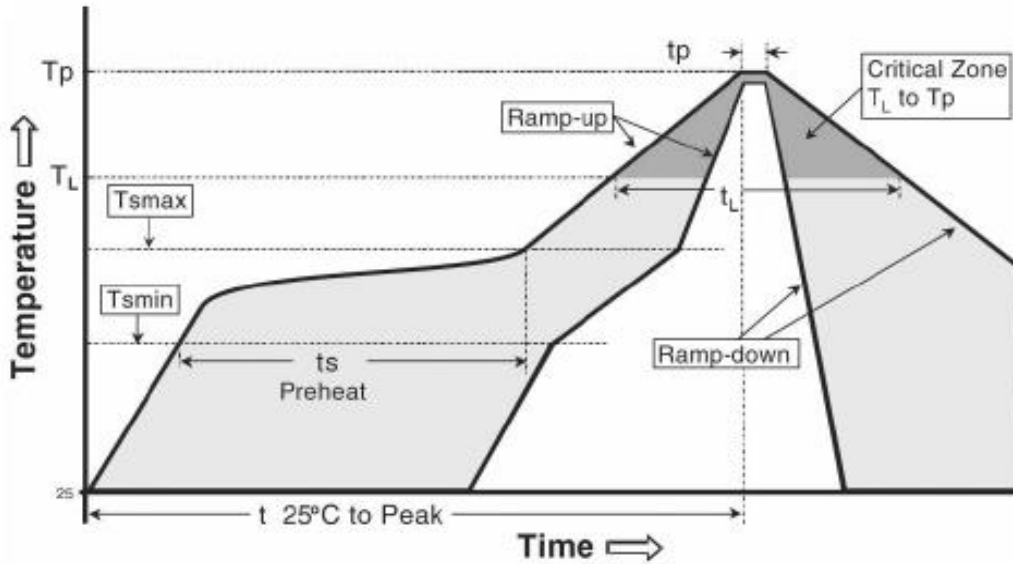


<SMJR-N-2-24>

Notes :

- 1) ①~③ : Thermal PAD (Recommended wide copper traces.)
- 2) ①~② : Electrical PAD do not connect to other devices(bleeder resistance – max. Power : 2W)
- 3) ③~③ : Electrical Isolated PAD.

Reflow Soldering Characteristics



Profile Feature	Pb-Free Assembly
Average ramp-up rate (T _{smax} to T _p)	3° C/second max.
Preheat - Temperature Min (T _{smin}) - Temperature Max (T _{smax}) - Time (T _{smin} to T _{smax}) (t _s)	150 °C 180 °C 80-120 seconds
Time maintained above: - Temperature (T _L) - Time (t _L)	217~220°C 80-100 seconds
Peak Temperature (T _p)	250~255°C
Time within 5°C of actual Peak Temperature (t _p) ₂	20-40 seconds
Ramp-down Rate	6 °C/second max.
Time 25°C to Peak Temperature	8 minutes max.
Atmosphere	Nitrogen (O ₂ <1000ppm)

Caution

- (1) Reflow soldering is recommended not to be done more than two times. In the case of more than 24 hours passed soldering after first, LEDs will be damaged.
- (2) Re-soldering should not be done after the LEDs have been soldered. If re-soldering is unavoidable, LED's characteristics should be carefully checked before and after such repair..
- (3) Do not put stress on the LEDs during heating.
- (4) After reflow, do not clean PCB by water or solvent.

SMT recommendation

- (1) Solder paste materials (SAC 305, No Cleaning Paste) → Senju M705-GRN360-KV

Marking Information

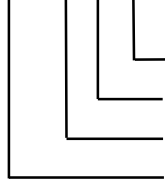


1. Seoul-semiconductor information

NANODRIVER

①

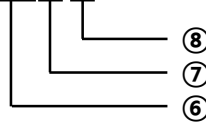
SMJR-N-1-16



- ① Product Name (Fixed 10 Digits)
- ② SMJR – driver(Fixed 4 Digits)
- ③ N – NANODRIVER(1 Digit)
- ④ 1 – 100V (1 Digit)
- ⑤ 16 – Maximum Power (2 Digits)

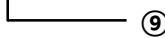
2. CM information

DA1720



- ⑥ DA – DATE (Fixed 2 Digits)
- ⑦ 17 – Production year(2 Digits)
- ⑧ 20 – Production week(2 Digits)
- ⑨ Lot numbers(11 Digits)

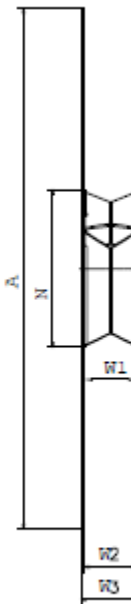
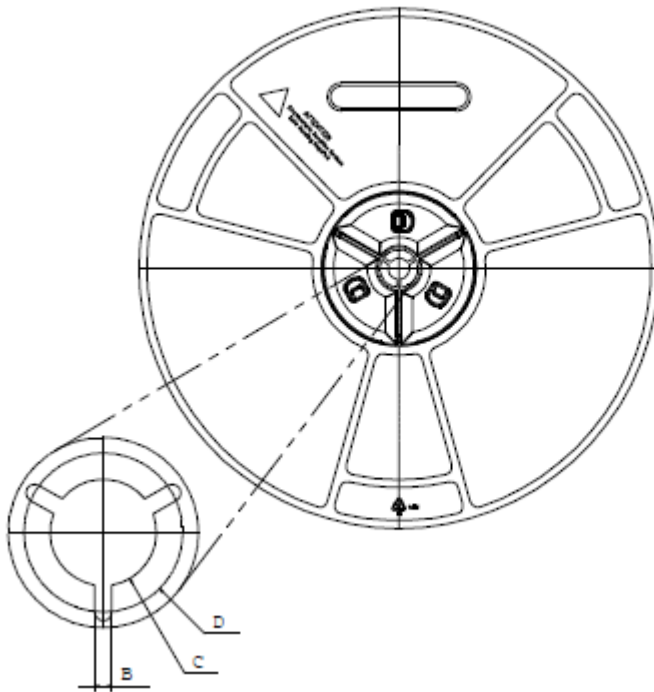
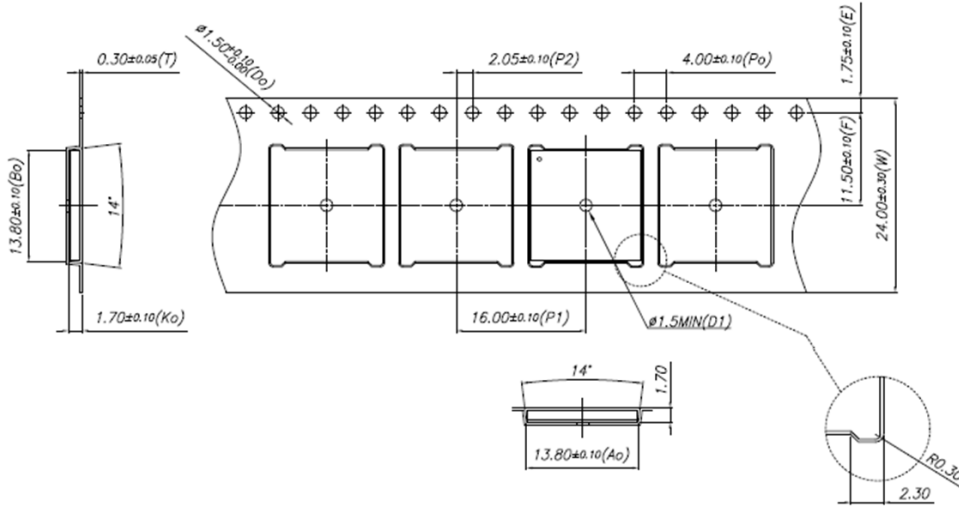
AHAYR2.3A05



②		③		④		⑤		
SMJR		N		Typ. Voltage		Maximum Power		
Mark	Explain code	Mark	Explain code	Mark	Min	Max	Mark	Max.
SMJR	SSC internal Code	N	NANODRIVER	1	100		16	16
							24	24

Emitter Tape & Reel Packaging

(hole pitch cumulative tolerance: ± 0.2 , Unit: mm)

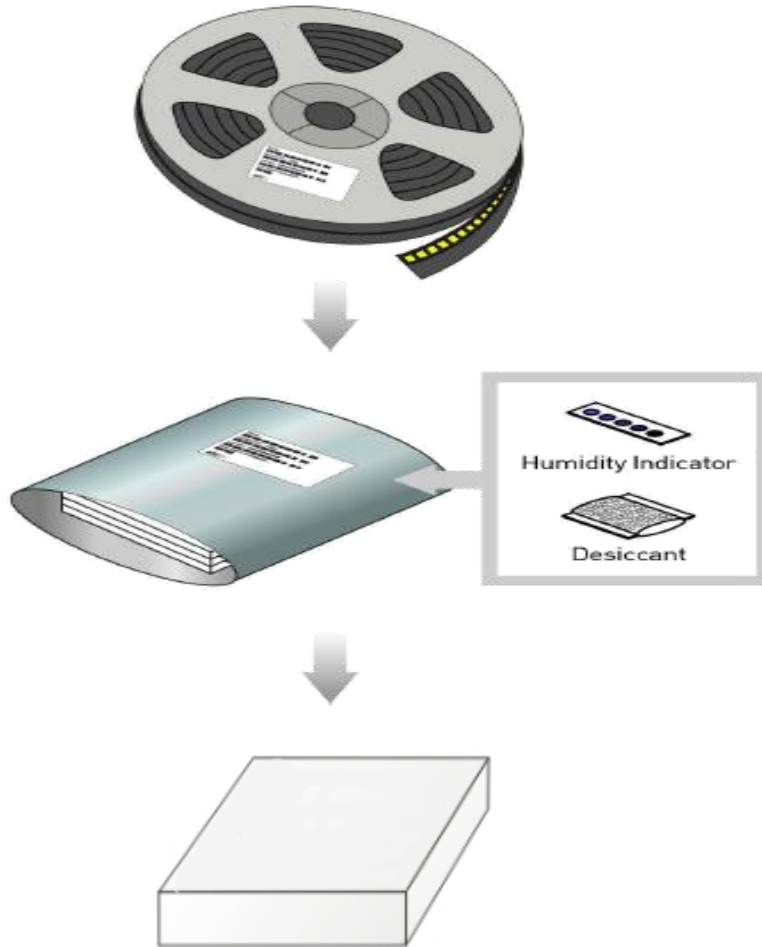


Size	24mm
A	330 ^{+0.2} _{-2.0}
B	1.5min
C	13.0 ^{+0.3} _{-0.2}
D	20.2min
N	100 ^{+3.0} _{-0.0}
W1	24.4 ^{+2.0} _{-2.0}
W2	24.4 ^{+2.0} _{-2.0}
W3	28.4 ^{+2.0} _{-2.0}

Notes :

- (1) Quantity : 2,000pcs/Reel
(empty slot possible in taping reel)
- (2) Cumulative Tolerance : Cumulative Tolerance/16 pitches to be ± 0.1 mm
- (3) Adhesion Strength of Cover Tape : Adhesion strength to be 0.1-0.7N when the cover tape is turned off from the carrier tape at the angle of 10° to the carrier tape
- (4) Package : P/N, Manufacturing data Code No. and quantity to be indicated on a damp proof package.

Packaging Information




- Inner box is included sealing AI-bag
- Inner box size (340 x 325 x 50mm)
- 2,000 PCS NANODRIVER packed per inner box




- Outer box is included 5ea inner box
- Outer box size (380 x 340 x 380mm)
- 10,000 PCS NANODRIVER packed per outer box

Label Information

Model No.	SMJR-N-1-XX (1)
LOT ID	XXXXXXXXXXXX
Quantity	XXXX
Date	YYMDDXXXXX-XXXXXXX
	SEOUL SEMICONDUCTOR CO.,LTD.

Notes :

- 1) The model number designation is explained as follow
 SMJR : Seoul Semiconductor internal code
 N : NANODRIVER
 1 : 100V
 XX: 16 : use up to 16W / 24 : use up to 24W
- 2) It is attached to the side of Inner and outer box

<p>TOTAL Quantity</p> <p> </p> <p>XXXX</p>
 SEOUL SEMICONDUCTOR CO.,LTD.

Notes :

- 1) It is attached to the side of the outer box.

Precaution for Use

- (1) Please review the NANODRIVER Application Note for proper protective circuitry usage.
- (2) Please note, NANODRIVER products run off of **NON-ISOLATED HIGH VOLTAGE**, therefore caution should be taken when working near the NANODRIVER products. Any fixture needs to ensure adequate protection from exposing any user to prevent electric shock.
- (3) Make sure proper discharge prior to starting work.
- (4) DO NOT touch any of the circuit board, components or terminals with body or metal while circuit is active.
- (5) Please do not add or change wires while NANODRIVER circuit is active.
- (6) Please do not assemble in conditions of high moisture and/or oxidizing gas such as Cl, H₂S, NH₃, SO₂, NO_x, etc.
- (7) Please do not make any modification on module.
- (8) Please be cautious when soldering to board so as not to create a short between different trace patterns.
- (9) Do not impact or place pressure on this product because even a small amount of pressure can damage the product. The product should also not be placed in high temperatures, high humidity or direct sunlight since the device is sensitive to these conditions.
- (10) When storing devices for a long period of time before usage, please following these guidelines:
 - * The devices should be stored in the anti-static bag that it was shipped in from Seoul-Semiconductor with opening.
 - * If the anti-static bag has been opened, re-seal preventing air and moisture from being present in the bag.
- (11) LEDs and IC are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS). The NANODRIVER product should also not be installed in end equipment without ESD protection.
- (12) LEDs and IC are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS). The NANODRIVER product should also not be installed in end equipment without ESD protection.
- (13) Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.
 - a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is the defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to an LEDs may cause the product to demonstrate unusual characteristics such as:

Precaution for Use

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event. One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:

- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

Environmental controls:

- Humidity control (ESD gets worse in a dry environment)

b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device. The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package
(If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package
(shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires
- This damage usually appears due to the thermal stress produced during the EOS event

c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:

- A surge protection circuit
- An appropriately rated over voltage protection device
- A current limiting device

Precaution for Use

d. MSL 3 (Moisture Sensitivity Level 3)

The detrimental effects of absorbed moisture in semiconductor packages during SMT assembly have been well documented in technical publications, manufacturers' literature and various industry standards. The purpose of this document is to identify areas of potential concern for end users and steps they should take to preclude problems.

Module manufacturers should bake and dry-pack all modules before shipment. The packing should include:

- A suitable MBB that restricts vapor transmission.
- Drying desiccant and HIC should be included in the bag.
- A MSL 3 label should be displayed prominently to caution that the bag contains moisture sensitive modules.

It is also recommended that module manufacturers highlight in their datasheet that the SMT modules are rated MSL 3.

- The end customer should be advised of the shelf life of modules in a sealed bag. It is recommended that shelf life should not exceed 12 months at <40°C and <90% RH.
- Upon opening of MBB, the end customer should check the HIC immediately; modules require baking before main board mounting if the HIC is >10% when read at 23°C ±5°C
- After MBB is opened, modules must go through reflow for main board assembly within 48 hours at factory conditions of <30°C/60% RH, or stored at <10% RH. If both of these conditions are not met, baking is required before main board mounting.
- If baking is required, modules should be baked for a minimum of 8 hours at 125°C.
- Any unused modules after the MBB has been open for more than 168 hours or not stored at <10% RH should be baked before any subsequent reflow and board assembly.
- Unused modules can either be baked and dry-packed first before storage, or they can be baked just before the next assembly onto main board. It is recommended that the former be practiced as it helps to prevent operator error from re-using modules without first baking. In both cases, the repacked modules should follow the guidelines in section 3A.
- Reworking Modules on NANODRIVER
 - 1) Before a module is removed from the main board, the main board should first be baked.
 - 2) Baking should be done for a minimum of 8 hours at 125°C.
 - 3) It is recommended that during removal, localized heating be used, and the maximum body temperature of device should not exceed 200°C.
 - 4) The replacement module should not have exceeded the specified floor life of 168 hours.



Company Information

Published by

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Company Information

Seoul Semiconductor (www.SeoulSemicon.com) manufactures and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, Home appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs.

The company's broad product portfolio includes a wide array of package and device choices such as Acrich and Acirch2, high-brightness LEDs, mid-power LEDs, side-view LEDs, and through-hole type LEDs as well as custom modules, displays, and sensors.

Legal Disclaimer

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Revision History

Revision	Date	Page	Remarks
1.0	Mar. 12 th , 2018	All	Initial release